

MEMORY I/O MAP

ADDRESS	MEMORY		
0000H	User Code Area		
5FFFH			
6000H	User Data and Stack Area NOTE: This is where the user stack should start.		
6FFFH			
6900H	Monitor Data and Stack Area		
69FFFH			
7000H	Memory Module Area		
AFFFH			
	NOTE: This is the Internal I/O Area.		
B000H	PORTA	Port A Data Register	
B001H	Reserved		
B002H	PIOC	Parallel I/O Control Register	
B003H	PORTC	Port C Data Register	
B004H	PORTB	Port B Data Register	
B005H	PORTCL	Port C Latched Data Register	
B006H	Reserved		
B007H	DDRC	Data Direction Register for Port C	
B008H	PORTD	Port D Data Register	
B009H	DDRD	Data Direction Register for Port D	
B00AH	PORTE	Port E Data Register	
B00BH	CFORC	Time Compare Force Register	
B00CH	OC1M	Output Compare 1 Mask Register	
B00DH	OC1D	Output Compare 1 Data Register	
B00EH, B00FH	TCNT	Timer Counter Register	
B010H, B011H	TIC1	Timer Input Capture Register 1	
B012H, B013H	TIC2	Timer Input Capture Register 2	
B014H, B015H	TIC3	Timer Input Capture Register 3	
B016H, B017H	TOC1	Timer Output Compare Register 1	
B018H, B019H	TOC2	Timer Output Compare Register 2	
B01AH, B01BH	TOC3	Timer Output Compare Register 3	
B01CH, B01DH	TOC4	Timer Output Compare Register 4	
B01EH, B01FH	TOC5	Timer Output Compare Register 5	
B020H	TCTL1	Timer Control Register 1	
B021H	TCTL2	Timer Control Register 2	
B022H	TMSK1	Main Timer Interrupt Mask Reg. 1	
B023H	TFLG1	Main Timer Interrupt Flag Reg. 1	
B024H	TMSK2	Misc. Timer Interrupt Mask Reg. 2	
B025H	TFLG2	Misc. Timer Interrupt Flag Reg. 2	
B026H	PACTL	Pulse Accumulator Control Register	
B027H	PACNT	Pulse Accumulator Count Register	
B028H	SPCR	SPI Control Register	
B029H	SPSR	SPI Status Register	
B02AH	SPDR	SPI Data Register	
B02BH	BAUD	SCI Baud Rate Control Register	
B02CH	SCCR1	SCI Control Register 1	
B02DH	SCCR2	SCI Control Register 2	
B02EH	SCSR	SCI Status Register	
B02FH	SCDR	SCI Data Register	
B030H	ADCTL	A/D Control/Status Register	
B031H	ADR1	A/D Result Register 1	
B032H	ADR2	A/D Result Register 2	
B033H	ADR3	A/D Result Register 3	
B034H	ADR4	A/D Result Register 4	
B035H	Reserved		
B036H	Reserved		
B037H	Reserved		
B038H	Reserved		
B039H	OPTION	System Configuration Options	
B03AH	COPRST	Arm/Reset COP Timer Circuitry	
B03BH	PProg	EEPROM Programming Register	
B03CH	HPRIO	Highest Priority Interrupt and Misc.	
B03DH	INIT	RAM and I/O Mapping Register	
B03EH	TEST1	Factory Test Register	
B03FH	CONFIG	Configuration Control Register	
B218H	Keyboard Column 3	NOTE: Keyboard Columns are not fully decoded.	
B228H	Keyboard Column 2	Use only the address specified.	
B230H	Keyboard Column 1		
B240H	Output Port		
B27FH			
B280H	Input Port		
B2BFH			
B2C0H	LCD Registers	NOTE: B2C0H is a Command Register and B2C1H is a Data Register. The Remaining Registers are undecoded	
B2FFH			
B300H	General I/O 0		
B33FH			
B340H	General I/O 1		
B37FH			
B380H	General I/O 2		
B3BFH			
B3C0H	General I/O 3		
B3FFH			
B400H	Unused		
BFFFH			
C000H	Monitor ROM Area		
FFFFH			