

Fixed-Point & Floating-Point Number Formats

CSC231—Assembly Language Week #14

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Reference

http://cs.smith.edu/dftwiki/index.php/ CSC231_An_Introduction_to_Fixed-_and_Floating-Point_Numbers

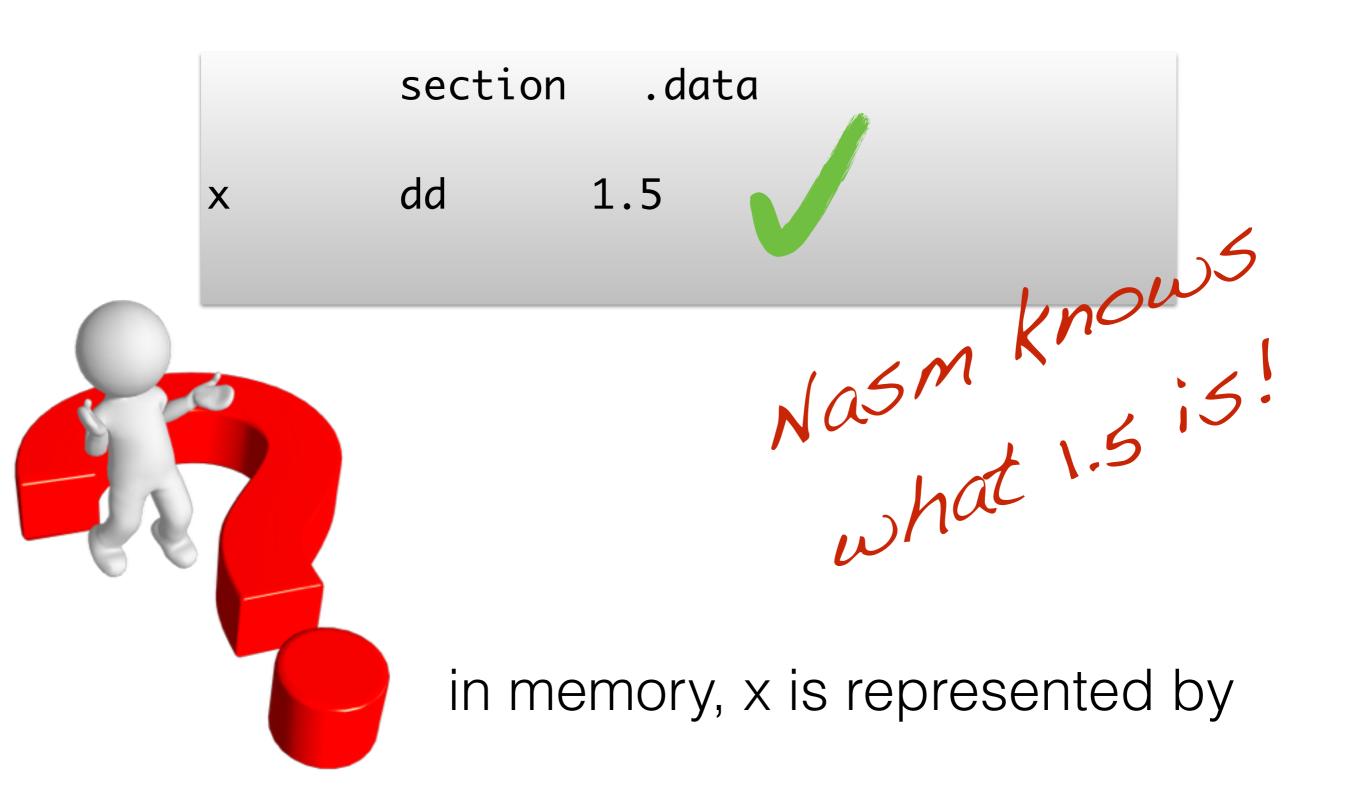


```
public static void main(String[] args) {
  int n = 10;
  int k = -20;
  float x = 1.50;
  double y = 6.02e23;
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public static void main(String[] args) {
  int n = 10;
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  float x = 1.50;
double y = 6.02e23;
```

Nasm knows what **1.5** is!



00111111 11000000 00000000 00000000 or 0x3FC00000

- **Fixed-Point Format**
- Floating-Point Format

Fixed-Point Format

- Used in very few applications, but programmers know about it.
- Some micro controllers (e.g. Arduino Uno) do not have Floating Point Units (FPU), and must rely on libraries to perform Floating Point operations (VERY SLOW)
- Can be used when storage is at a premium (can use small quantity of bits to represent a real number)

Review Decimal System

$$123.45 = 1x10^{2} + 2x10^{1} + 3x10^{0} + 4x10^{-1} + 5x10^{-2}$$

Decimal Point

Can we do the same in binary?

• Let's do it with **unsigned numbers** first:

$$1101.11 = 1x2^3 + 1x2^2 + 0x2^1 + 1x2^0 + 1x2^{-1} + 1x2^{-2}$$

Binary Point

Can we do the same in binary?

Let's do it with unsigned numbers first:

$$1101.11 = 1x2^{3} + 1x2^{2} + 0x2^{1} + 1x2^{0} + 1x2^{-1} + 1x2^{-2}$$

$$= 8 + 4 + 1 + 0.5 + 0.25$$

$$= 13.75$$

OBSERVATIONS

- If we know where the binary point is, we do not need to "store" it anywhere. (Remember we used a bit to represent the +/- sign in 2's complement.)
- A format where the binary/decimal point is fixed between 2 groups of bits is called a fixed-point format.

Definition

- A number format where the numbers are unsigned and where we have a integer bits (on the left of the decimal point) and b fractional bits (on the right of the decimal point) is referred to as a U(a,b) fixedpoint format.
- Value of an N-bit binary number in U(a,b):

$$x = (1/2^b) \sum_{n=0}^{N-1} 2^n x_n$$

Exercise 1 Spical Rival American Strong Ram

$$x = 1011 1111 = 0xBF$$

What is the value represented by x in *U(4,4)*

What is the value represented by x in *U(7,3)*



Exercise 2 mestion, exam

• z = 0000000100000000

• y = 000000100000000

• V = 00000010 10000000

 What values do z, y, and v represent in a *U(8,8)* format?

Exercise 3 question, exam

• What is 12.25 in *U(4,4)*? In *U(8,8)*?



What about **Signed** Numbers?

Observation #1

 In an N-bit, unsigned integer format, the weight of the MSB is 2N-1

nybble	Unsigned

Пурріе	Unsigned
0000	+0
0001	+1
0010	+2
0011	+3
0100	+4
0101	+5
0110	+6
0111	+7
1000	+8
1001	+9
1010	+10
1011	+11
1100	+12
1101	+13
1110	+14
1111	+15

$$N = 4$$

 $2^{N-1} = 2^3 = 8$

Observation #2

 In an N-bit signed 2's complement, integer format, the weight of the MSB is -2^{N-1}

nybble	2's complement
0000	+0
0001	+1
0010	+2
0011	+3
0100	+4
0101	+5
0110	+6
0111	+7
1000	-8
1001	-7
1010	-6
1011	-5
1100	-4
1110	-3
1111	-2

$$N=4$$

 $-2^{N-1}=-2^3=-8$

Fixed-Point Signed Format

- Fixed-Point signed format = sign bit + a integer bits +
 b fractional bits = N bits = A(a, b)
- N = number of bits = 1 + a + b
- Format of an N-bit A(a, b) number:

$$x = (1/2^b) \left[-2^{N-1} x_{N-1} + \sum_{0}^{N-2} 2^n x_n \right],$$

Examples in A(7,8)

- $00000001\ 00000000 = 00000001\ .\ 00000000 = ?$
- 10000001 00000000 = 10000001 . 00000000 = ?
- $0000010\ 00000000 = 0000010\ .\ 00000000 = ?$
- $10000010\ 000000000 = 1000010\ .\ 000000000 = ?$
- $00000010\ 10000000 = 00000010\ .\ 10000000 = ?$
- $10000010\ 100000000 = 10000010\ .\ 100000000 = ?$

Examples in A(7,8)

- 00000001 00000000 = 00000001 .00000000 = 1d
- $100000001\ 00000000 = 10000001\ .\ 00000000 = -128 + 1 = -127d$
- $00000010\ 00000000 = 0000010\ .\ 00000000 = 2d$
- $10000010\ 00000000 = 1000010\ .\ 00000000 = -128 + 2 = -126d$
- $00000010\ 10000000 = 00000010\ .\ 10000000 = 2.5d$
- $10000010\ 100000000 = 100000010\ .\ 100000000 = -128 + 2.5 = -125.5d$

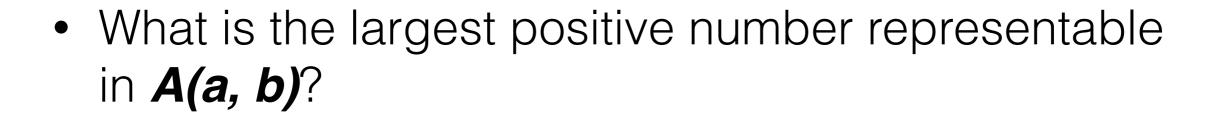
Exercises

- What is -1 in **A(7,8)**?
- What is -1 in A(3,4)?
- What is 0 in **A(7,8)**?
- What is the smallest number one can represent in A(7,8)?
- The largest in **A(7,8)**?



Exercises

- What is the largest number representable in *U(a, b)*?
- What is the smallest number representable in *U(a, b)*?



 What is the smallest negative number representable in *A(a, b)*?





- Fixed-Point Format
 - Definitions
 - Range
 - Precision
 - Accuracy
 - Resolution
- Floating-Point Format

Range

- Range = difference between most positive and most negative numbers.
- Unsigned Range: The range of U(a, b) is $0 \le x \le 2^a - 2^{-b}$
- Signed Range: The range of A(a, b) is $-2^a \le x \le 2^a - 2^{-b}$

2 different 2 different definition5

Precision

Precision = b, the number of fractional bits https://en.wikibooks.org/wiki/Floating Point/Fixed-Point Numbers

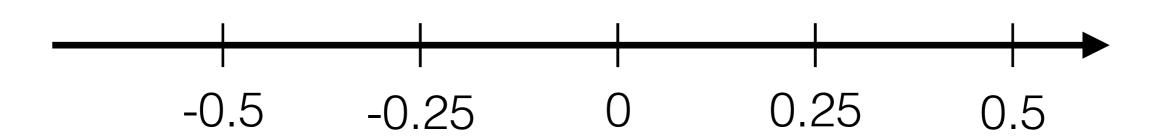
Precision = N, the total number of bits

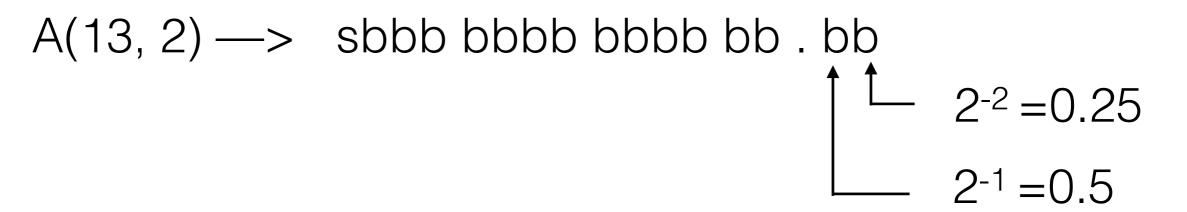
Randy Yates, Fixed Point Arithmetic: An Introduction, Digital Signal Labs, July 2009. http://www.digitalsignallabs.com/fp.pdf

Resolution

- The resolution is the smallest non-zero magnitude representable.
- The resolution is the size of the intervals between numbers represented by the format
- Example: **A(13, 2)** has a resolution of 0.25.

 $A(13, 2) \longrightarrow sbbb bbb bbb bb . bb$ $2^{-2} = 0.25$ $2^{-1} = 0.5$

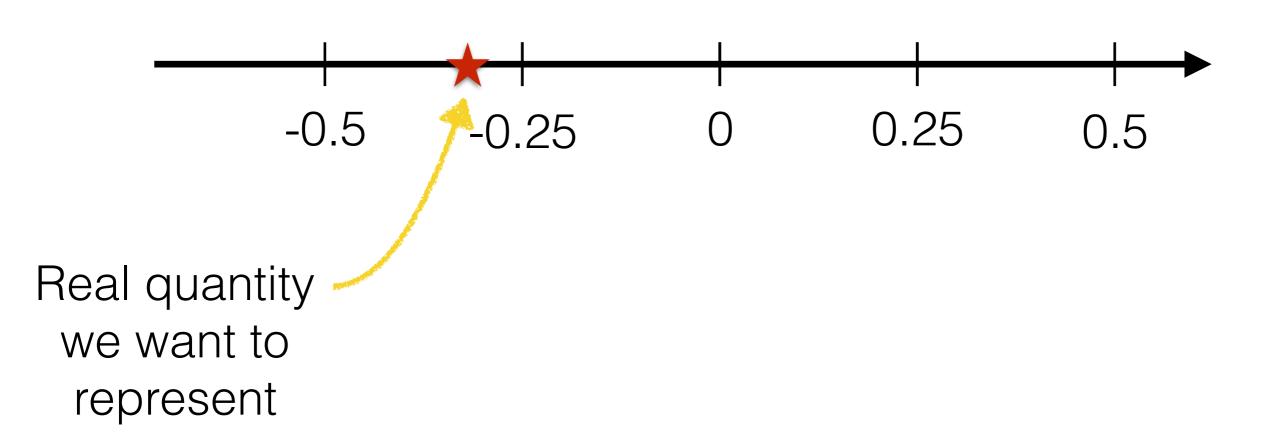


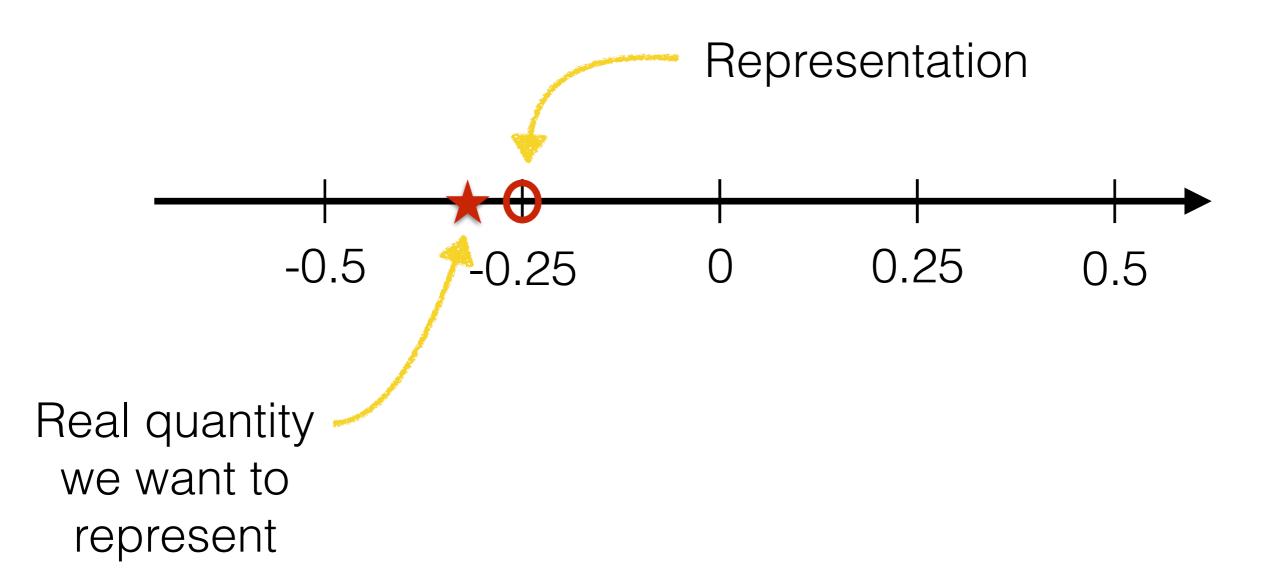




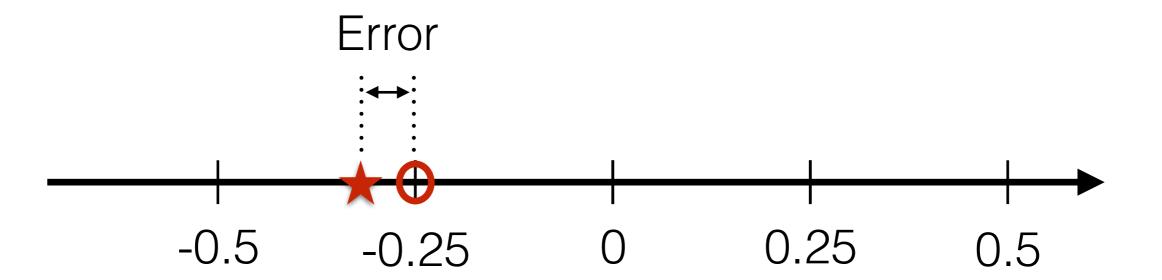
Accuracy

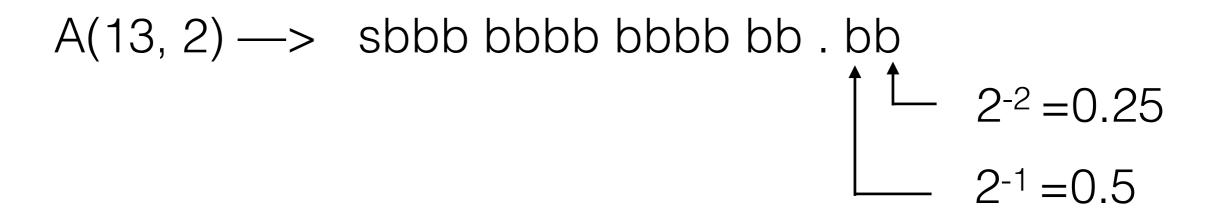
- The accuracy is the largest magnitude of the difference between a number and its representation.
- Accuracy = 1/2 Resolution

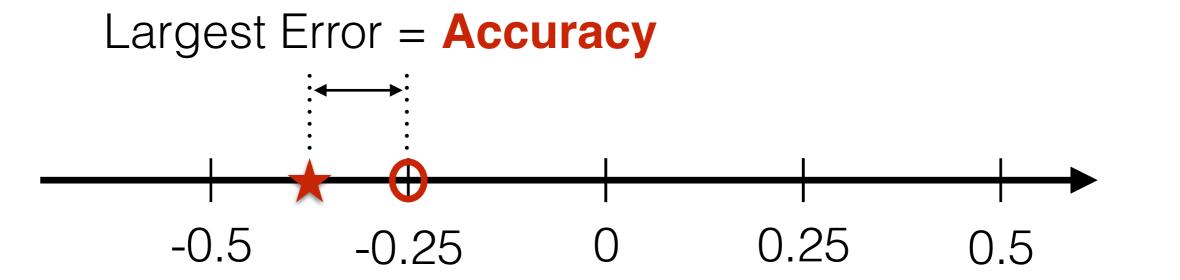




 $A(13, 2) \longrightarrow sbbb bbb bbb bb . bb$ $2^{-2} = 0.25$ $2^{-1} = 0.5$









Questions in search of answers...

- What is the accuracy of an U(7,8) number format?
- How good is U(7,8) at representing small numbers versus representing larger numbers? In other words, is the format treating small numbers better than large numbers, or the opposite?



Questions in search of answers...

U(7,8) resolution = $2^{-8} = 0.00390625$ $accuracy = 2^{-9} = 0001953125$

- What is the accuracy of an U(7,8) number format?
- How good is U(7,8) at representing small numbers versus representing larger numbers? In other words, is the format treating small numbers better than large numbers, or the opposite?

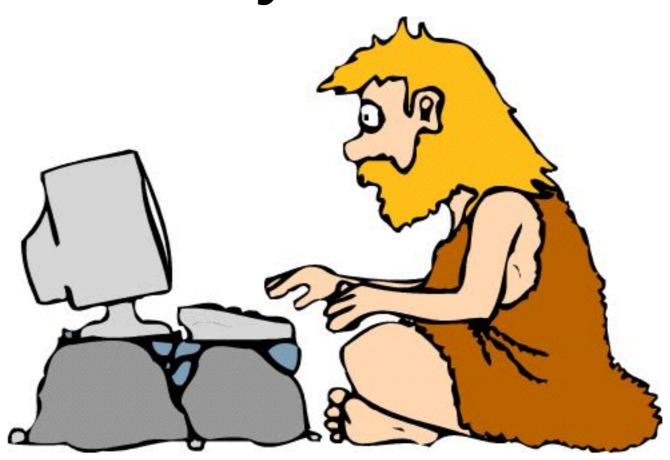
- Fixed-Point Format
- **Floating-Point Format**



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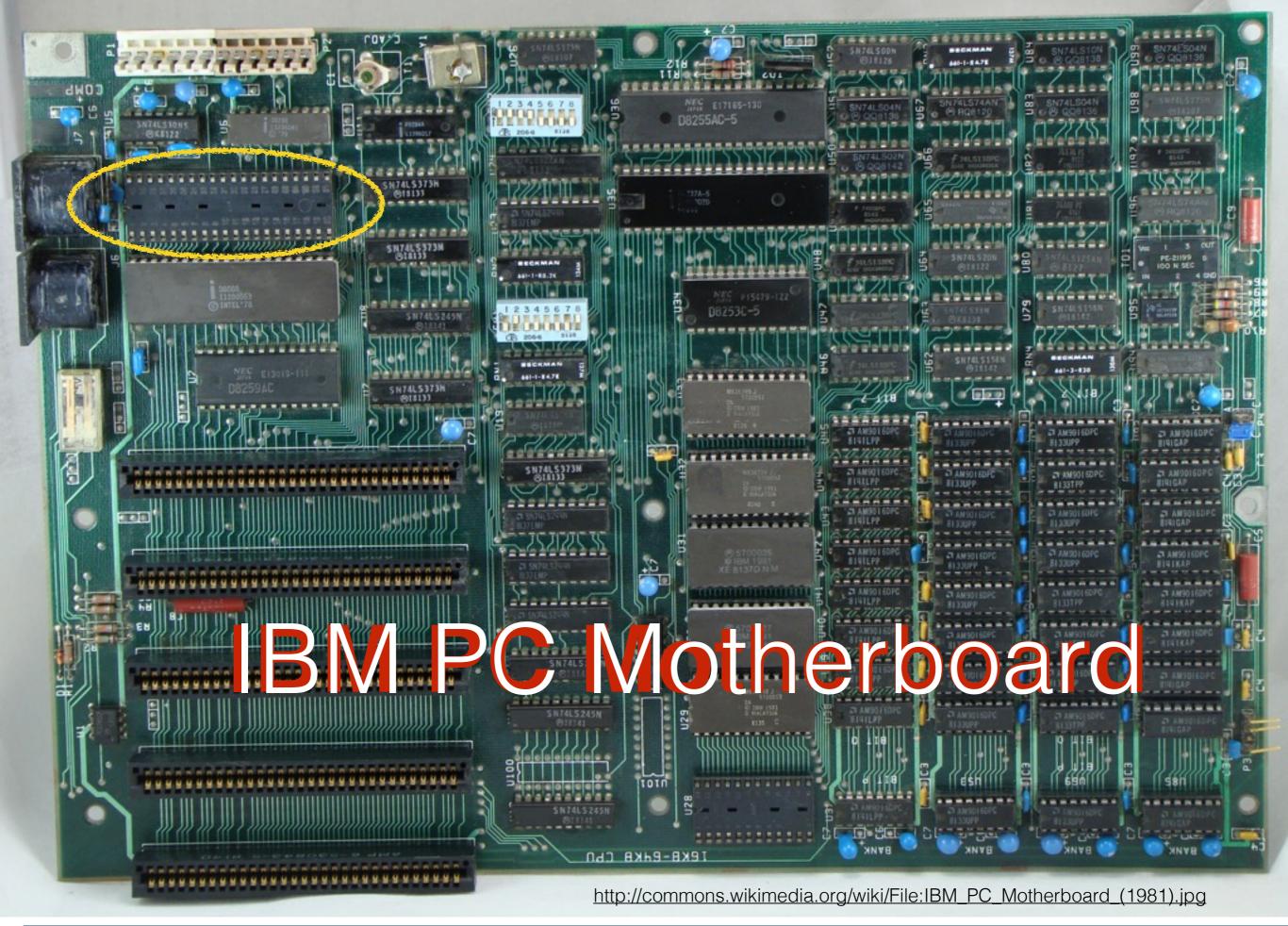
IEEE Floating-Point Number Format

A bit of history...



http://datacenterpost.com/wp-content/uploads/2014/09/Data-Center-History.png

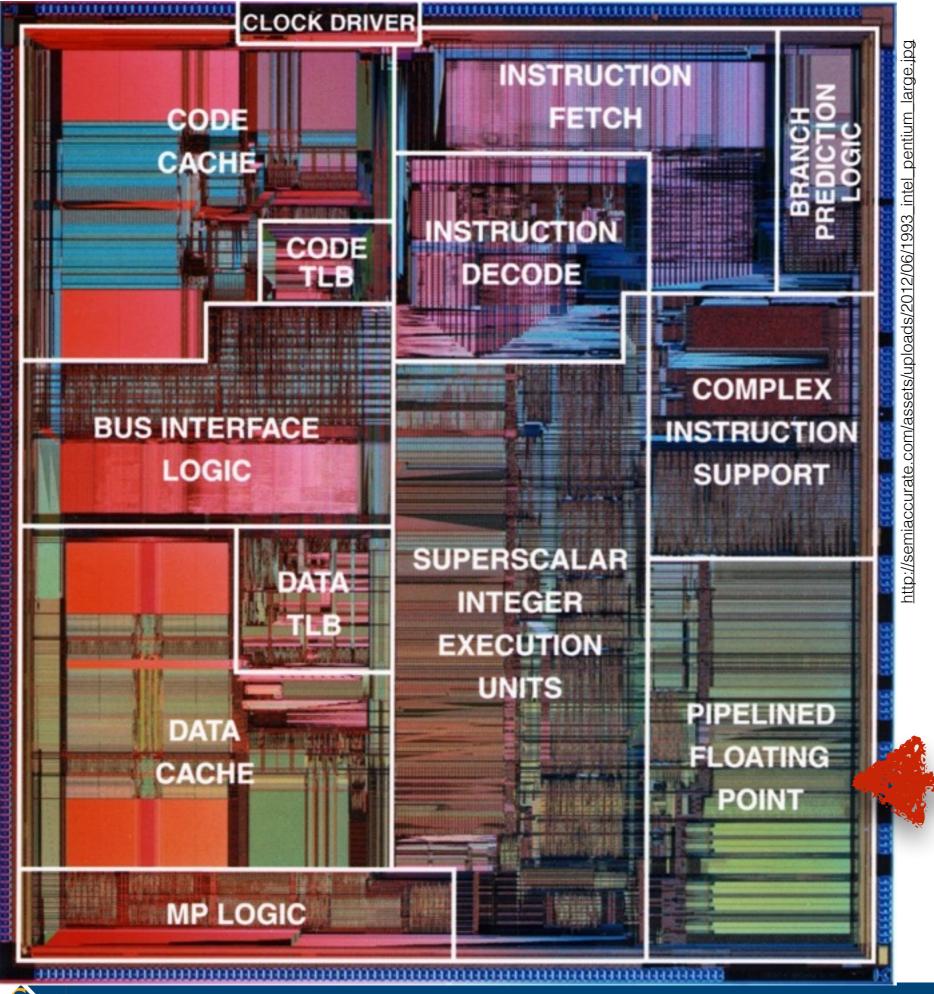
- 1960s, 1970s: many different ways for computers to represent and process real numbers. Large variation in way real numbers were operated on
- 1976: Intel starts design of first hardware floatingpoint co-processor for 8086. Wants to define a standard
- 1977: Second meeting under umbrella of Institute for Electrical and Electronics Engineers (IEEE).
 Mostly microprocessor makers (IBM is observer)
- Intel first to put whole math library in a processor



Intel Coprocessors

	Midshally	Intel
Processor	Year	Description
8087	1980	Numeric coprocessor for 8086 and 8088 processors.
80C187	19??	Math coprocessor for 80C186 embedded processors.
80287	1982	Math coprocessor for 80286 processors.
80387	1987	Math co-processor for 80386 processors.
80487	1991	Math co-processor for SX versions of 80486 processors.
Xeon Phi	2012	Multi-core co-processor for Xeon CPUs.
	WHAT PREPARE	





(Early)
Intel Pentium

Integrated Coprocessor

Some Processors that do not contain FPUs

- Some ARM processors
- Arduino Uno
- Others

Some Processors that do not contain FPUs

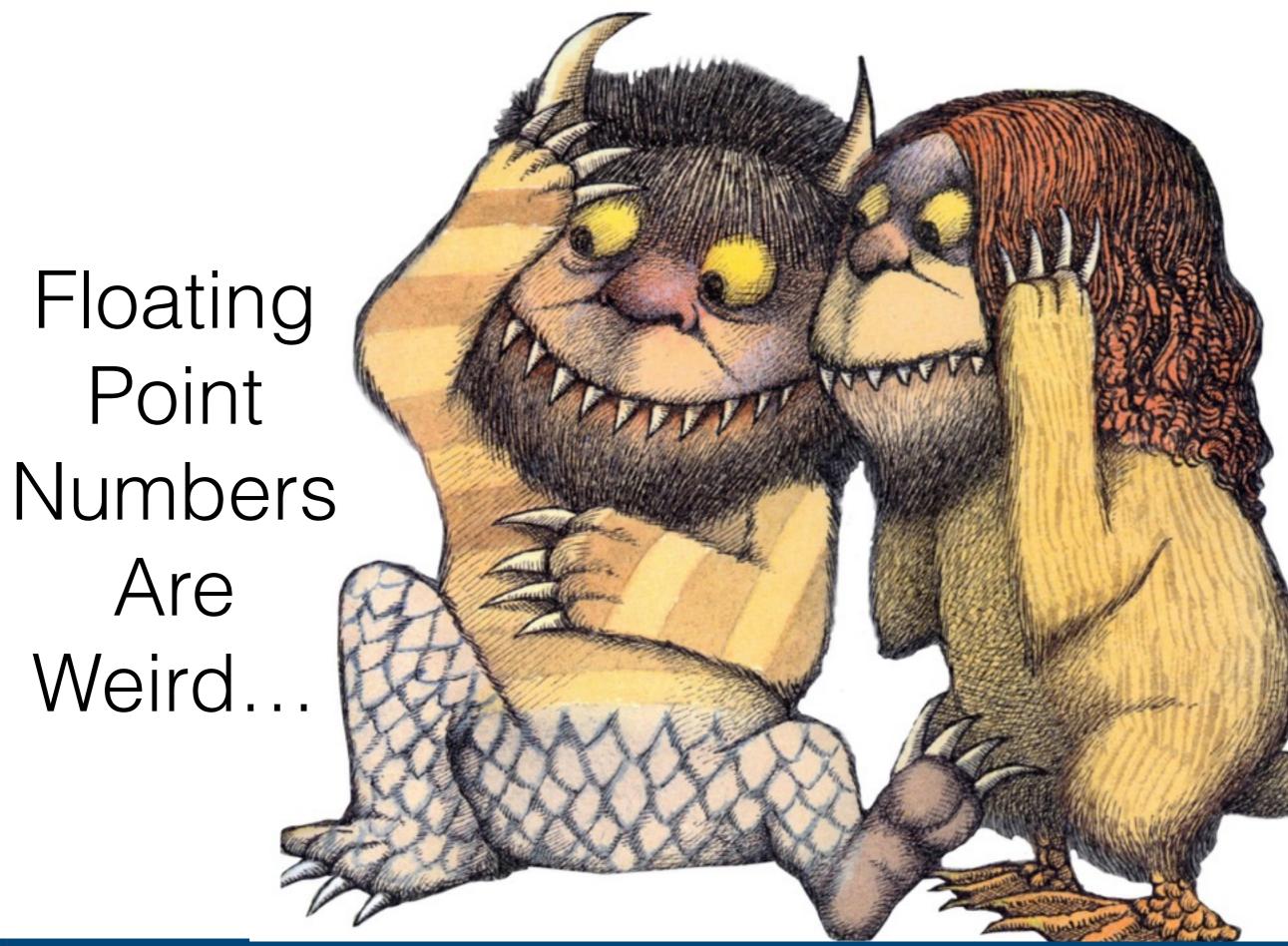
Few people have heard of ARM Holdings, even though sales of devices containing its flavor of chips are projected to be 25 times that of Intel. The chips found in **99 percent** of the world's smartphones and tablets are ARM designs. **About 4.3 billion people, 60 percent of the world's population, touch a device carrying an ARM chip each day**.

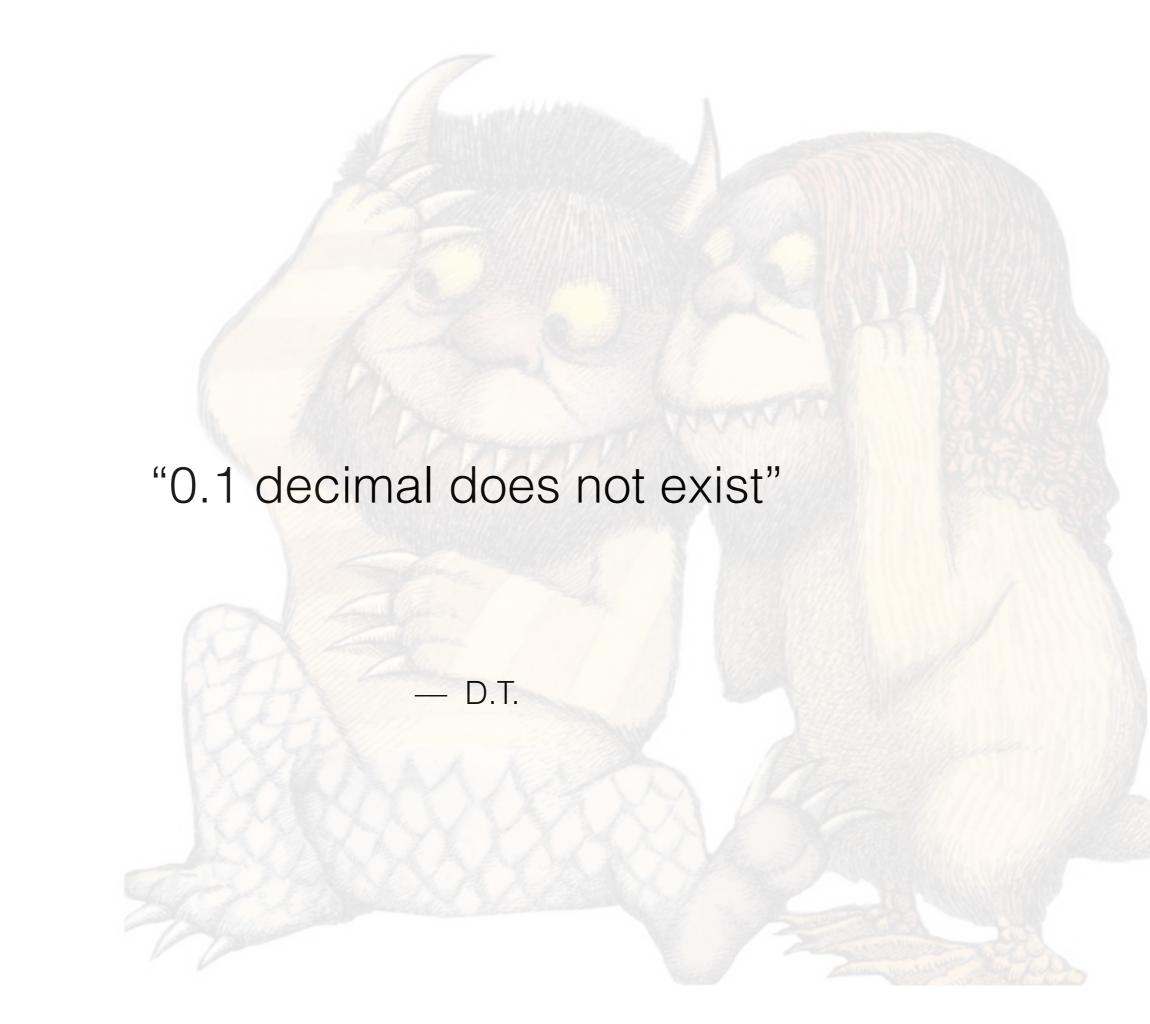
Ashlee Vance, Bloomberg, Feb 2014

How Much Slower is Library vs FPU operations?

- Cristina Iordache and Ping Tak Peter Tang, "An Overview of Floating-Point Support and Math Library on the Intel XScale Architecture", In *Proceedings IEEE Symposium* on Computer Arithmetic, pages 122-128, 2003
- http://stackoverflow.com/questions/15174105/
 performance-comparison-of-fpu-with-software-emulation

Library-emulated FP operations = 10 to 100 times slower than hardware FP operations executed by FPU





```
import java.util.*;
public class SomeFloats {
        public static void main(String args□) {
            float x = 6.02E23f,
               y = -0.000001f
                z = 1.23456789E-19f
                t = -1.0f
                u = 80000000000f;
            System.out.println( "\nx = " + x
                                + "\ny = " + y
                                + "\nz = " + z
                                + "\nt = " + t
                               + "\nu = " + u );
}
```

```
import java.util.*;
public class SomeFloats {
        public static void main(String args□) {
            float x = 6.02E23f,
               y = -0.000001f
               z = 1.23456789E-19f
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            System.out.println( "\nx = " + x
                                + "\ny = " + y
                                + "\nz = " + z
                                + "\nt = " + t
                                + "\nu = " + u );
}
```

231b@aurora ~/handout \$ java SomeFloats

```
x = 6.02E23
y = -1.0E-6
z = 1.2345678E-19
t = -1.0
u = 8.0E9
```

 $= 12.30 \cdot 10^{-1}$

 $= 123.0 \cdot 10^{-2}$

 $= 0.123 \cdot 10^{1}$

IEEE Format

- 32 bits, single precision (floats in Java)
- 64 bits, double precision (doubles in Java)
- 80 bits*, extended precision (C, C++)



^{*80} bits in assembly = 1 Tenbyte

1.011001 x 2⁴

1.011001 x 2⁴

 1.011001×2^{100}

1.011001 x 2⁴

 $+ 1.011001 \times 2^{100}$

10110.01 1.011001×2^{4} + 1.011001 x 2¹⁰⁰ 011001 100

0 011001 100

0 011001 100

In Decimal

```
1234.56
```

 $1234.56 \times 10 = 12345.6$

 $12345.6 \times 10 = 123456.0$

1234.56

1234.56 / 10 = 123.456

123.456 / 10 = 12.3456



In Decimal

In Binary

1234.56

 $1234.56 \times 10 = 12345.6$ $12345.6 \times 10 = 123456.0$

1234.56

1234.56 / 10 = 123.456123.456 / 10 = 12.3456

101.11

 $101.11 \times 2 = 1011.1$ $1011.1 \times 2 = 10111.0$

101.11

101.11 / 2 = 10.11110.111 / 2 = 1.0111

In Decimal

In Binary

1234.56

 $1234.56 \times 10 = 12345.6$

 $12345.6 \times 10 = 123456.0$

1234.56

1234.56 / 10 = 123.456

123.456 / 10 = 12.3456

101.11

 $101.11 \times 2 = 1011.1$ $1011.1 \times 2 = 10111.0$

101.11

101.11 / 2 = 10.11110.111 / 2 = 1.0111

=5.75d=11.50d=23.00d

=5.75d=2.875d=1.4375d

 $--101.11 \times 10 = 1011.1$

In Decimal

1234.56

 $1234.56 \times 10 = 12345.6$

 $12345.6 \times 10 = 123456.0$

1234.56

1234.56 / 10 = 123.456

123.456 / 10 = 12.3456

In Binary

101.11

 $101.11 \times 2 = 1011.1$

 $1011.1 \times 2 = 10111.0$

101.11

101.11 / 2 = 10.111

10.111 / 2 = 1.0111

=5.75d

=11.50d

=23.00d

=5.75d

=2.875d

=1.4375d

Observations

- +/- is the sign. It is represented by a bit, equal to 0 if the number is positive, 1 if negative.
- the part 1.bbbbbbb....bbb is called the mantissa
- the part bbb...bb is called the exponent
- 2 is the base for the exponent (could be different!)
- the number is **normalized** so that its binary point is moved to the right of the leading 1.
- because the leading bit will always be 1, we don't need to store
 it. This bit will be an implied bit.

IEEE 754 CONVERTER This page allows you to convert between the decimal representation of numbers (like "1.02") and the binary format used by all modern CPUs (IEEE 754 floating point). The conversion is limited to single precision numbers (32 Bit). The purpose of this webpage is to help you understand floating point numbers. IEEE 754 Converter (JavaScript), V0.12 Note: This JavaScript-based version is still under development, please report errors here. Sign Exponent Mantissa 2-4 1.600000023841858 Value: +1 Encoded as: 0 123 5033165 Binary: **Decimal Representation** 0.1 Binary Representation 00111101110011001100110011001101 Hexadecimal Representation 0x3dcccccd After casting to double precision 0.10000000149011612

http://www.h-schmidt.net/FloatConverter/IEEE754.html

Interlude...

```
for ( double d = 0; d != 0.3; d += 0.1 )
     System.out.println( d );
```









Normalization (in decimal)

(normal = standard form)

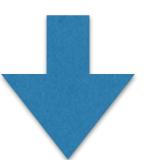
$$y = 123.456$$



 $y = 1.23456 \times 10^2$

Normalization (in binary)

y = 1000.100111 (8.609375d)



 $y = 1.000100111 \times 2^3$

Normalization (in binary)

y = 1000.100111



decimal

 $y = 1.000100111 & 2^3$

Normalization (in binary)

y = 1000.100111



decimal

 $y = 1.000100111 \times 2^3$

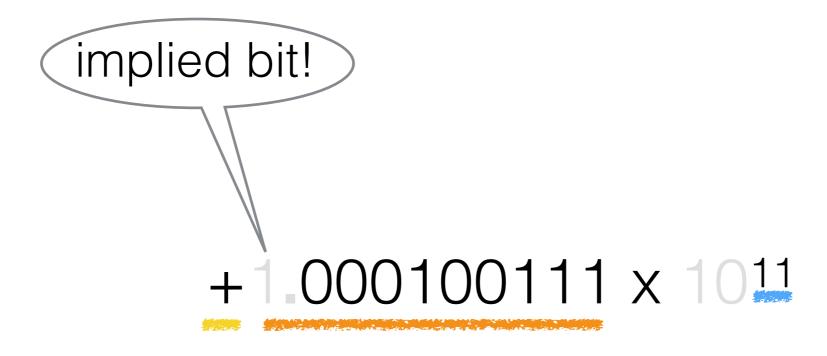
binary

 $y = 1.000100111 \times 10^{11}$



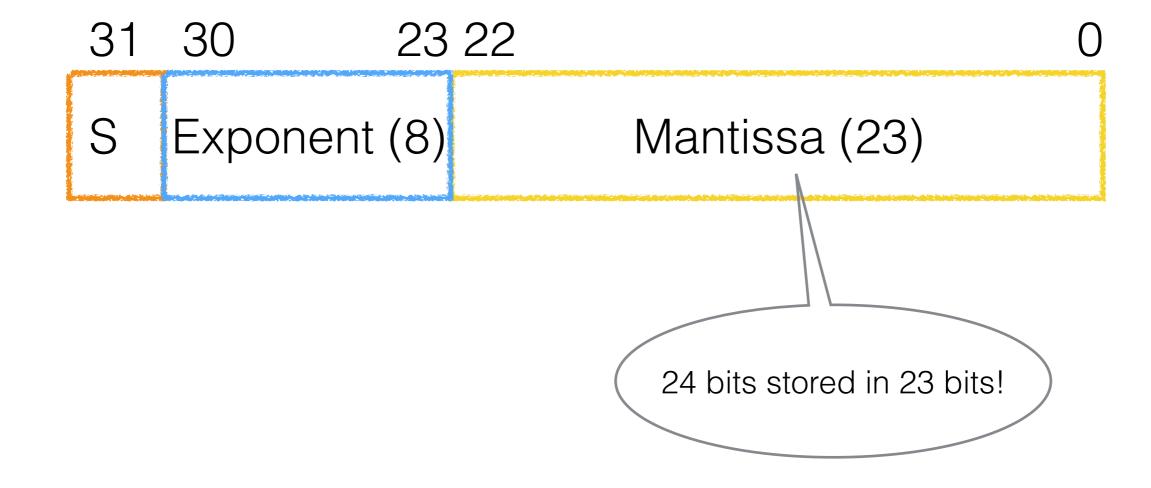
+1.000100111 x 1011

1000100111 sign mantissa exponent But, remember, all* numbers have a leading 1, so, we can pack the bits even more efficiently!



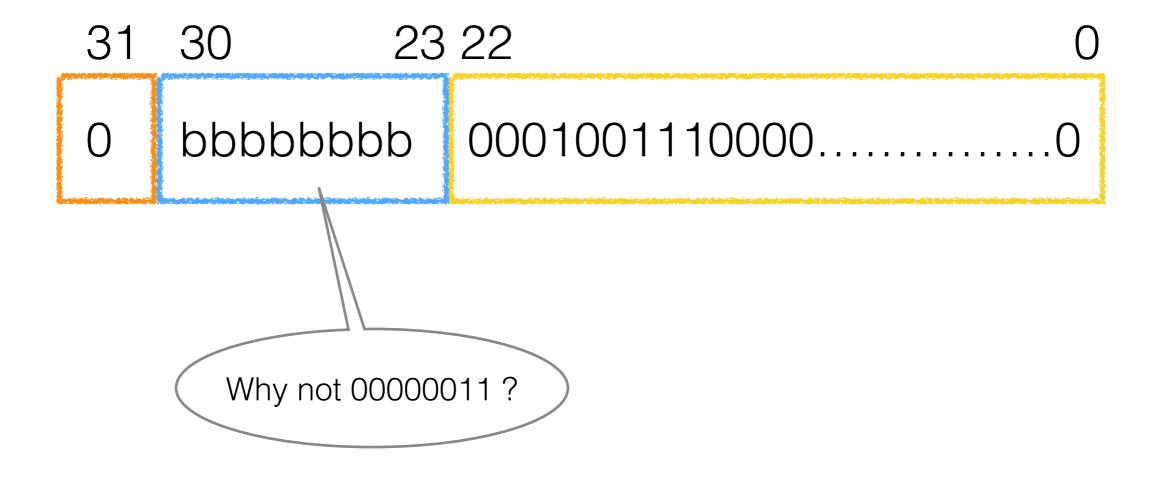


IEEE Format



y = 1000.100111 $y = 1.000100111 2^3$ $y = 1.000100111 10^{11}$ 30 23 22 31 0001001110000.....0 bbbbbbb 0

$y = 1.000100111 \times 10^{11}$



How is the exponent coded?

bbbbbbbb

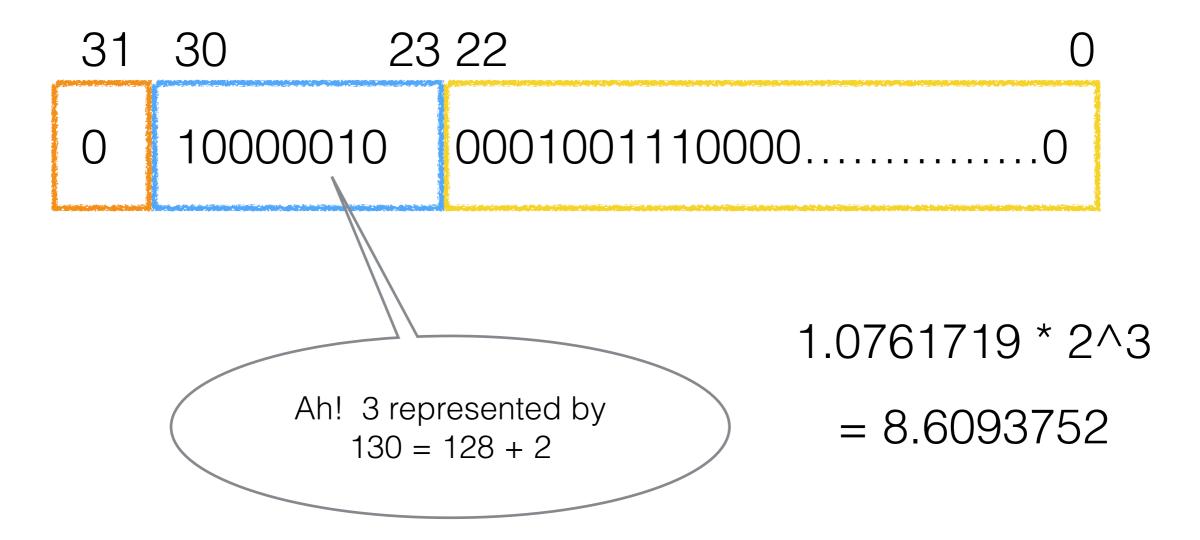
real exponent	stored exponent	Comments
-126	0	Special Case #1
-126	1	
-125	2	
-124	3	
-123	4	
•		
-1	126	
0	127	
1	128	
2	129	
3	130	
	•	
•	•	
127	254	
128	255	Special Case #2

bias of 127

ddddddd

real exponent	stored exponent	Comments
-126	0	Special Case #1
-126	1	
-125	2	
-124	3	
-123	4	
-1	126	
0	127	
1	128	
2	129	•
3	130	
•	•	
•	•	
127	254	
128	255	Special Case #2

$y = 1.000100111 \times 10^{11}$



Verification 8.6093752 in IEEE FP?

IEEE 754 CONVERTER This page allows you to convert between the decimal representation of numbers (like "1.02") and the binary format used by all modern CPUs (IEEE 754 floating point). The conversion is limited to single precision numbers (32 Bit). The purpose of this webpage is to help you understand floating point numbers. IEEE 754 Converter (JavaScript), V0.12 Note: This JavaScript-based version is still under development, please report errors here. Mantissa Sign Exponent +1 Value: 1.600000023841858 Encoded as: 123 5033165 Binary: **Decimal Representation** 0.1 Binary Representation 00111101110011001100110011001101 Hexadecimal Representation 0x3dcccccd After casting to double precision 0.10000000149011612

http://www.h-schmidt.net/FloatConverter/IEEE754.html

Exercises

- How is 1.0 coded as a 32-bit floating point number?
- What about 0.5?
- 1.5?
- -1.5?



 what floating-point value is stored in the 32-bit number below?

what about 0.1?



0.1 decimal, in 32-bit precision, IEEE Format:

0 01111011 1001100110011001101

0.1 decimal, in 32-bit precision, IEEE Format:

0 01111011 1001100110011001101

Value in double-precision: **0.1000000149011612**

NEVER NEVER COMPARE FLOATS OR DOUBLES FOR EQUALITY N-E-V-E-RI

```
for ( double d = 0; d != 0.3; d += 0.1 )
     System.out.println( d );
```







bbbbbbb

real exponent	stored exponen	t Comments
-126	0	Special Case #1
-126	1	
-125	2	
-124	3	
-123	4	
•		
-1	126	
0	127	
1	128	
2	129	
3	130	
127	254	
128	255	Special Case #2

Special Cases

Zero

- Why is it special?

ddddddd

real exponent	stored exponent	Comments
-126	0	Special Case #1
-126	1	
-125	2	
-124	3	
-123	4	
-1	126	
0	127	
1	128	
2	129	
3	130	
127	254	
128	255	Special Case #2

if mantissa is 0: number = **0.0**

Very Small Numbers

- Smallest numbers have stored exponent of 0.
- In this case, the implied 1 is omitted, and the exponent is -126 (not -127!)

bbbbbbb

real exponent	stored exponent	Comments
-126	0	Special Case #1
-126	1	
-125	2	
-124	3	
-123	4	
-1	126	
0	127	
1	128	
2	129	
3	130	
127	254	
128	255	Special Case #2

if mantissa is 0:
number = **0.0**if mantissa is !0:
no hidden 1

Very Small Numbers

0 I 00000000 I 001000...000
+
$$(2^{-126}) \times (0.001)$$
 binary
+ $(2^{-126}) \times (0.125) = 1.469 \times 10^{-39}$

bbbbbbb

real exponent	stored exponent	Comments
-126	0	Special Case #1
-126	1	
-125	2	
-124	3	
-123	4	
-1	126	
0	127	
1	128	
2	129	
3	130	
127	254	
128	255	Special Case #2

Special Cases



- stored exponent = 1111 1111
- if the mantissa is = 0

• stored exponent = 1111 1111

• if the mantissa is = 0 → +/- ∞



- stored exponent = 1111 1111
- if the mantissa is = 0
- if the mantissa is != 0

- stored exponent = 1111 1111
- if the mantissa is = 0 → +/- ∞
- if the mantissa is != 0 NaN = Not-a-Number

stored exponent = 1111 1111

• if the mantissa is = 0 ==> +/- ∞

if the mantissa is != 0 ==> NaN



NaN is sticky!

0 11111111 100000100000000000000000 = NaN

Operations that create NaNs (http://en.wikipedia.org/wiki/NaN):

- The divisions 0/0 and ±∞/±∞
- The multiplications 0×±∞ and ±∞×0
- The **additions** $\infty + (-\infty)$, $(-\infty) + \infty$ and equivalent subtractions
- The square root of a negative number.
- The **logarithm** of a negative number
- The inverse sine or cosine of a number that is less than -1 or greater than +1

```
// http://stackoverflow.com/questions/2887131/when-can-java-produce-a-nan
import java.util.*;
                                                                  Generating
import static java.lang.Double.NaN;
import static java.lang.Double.POSITIVE_INFINITY;
                                                                                     NaNs
import static java.lang.Double.NEGATIVE_INFINITY;
public class GenerateNaN {
    public static void main(String args[]) {
        double[] allNaNs = { 0D / 0D,
                POSITIVE_INFINITY / POSITIVE_INFINITY,
                POSITIVE_INFINITY / NEGATIVE_INFINITY,
                NEGATIVE_INFINITY / POSITIVE_INFINITY,
                NEGATIVE_INFINITY / NEGATIVE_INFINITY,
                0 * POSITIVE_INFINITY,
                0 * NEGATIVE_INFINITY,
                Math.pow(1, POSITIVE_INFINITY),
                POSITIVE_INFINITY + NEGATIVE_INFINITY,
                NEGATIVE_INFINITY + POSITIVE_INFINITY,
                POSITIVE_INFINITY - POSITIVE_INFINITY,
                NEGATIVE_INFINITY - NEGATIVE_INFINITY,
                Math.sqrt(-1),
                Math.log(-1),
                Math.asin(-2),
                Math.acos(+2),  };
        System.out.println(Arrays.toString(allNaNs));
        System.out.println(NaN == NaN);
        System.out.println(Double.isNaN(NaN));
```

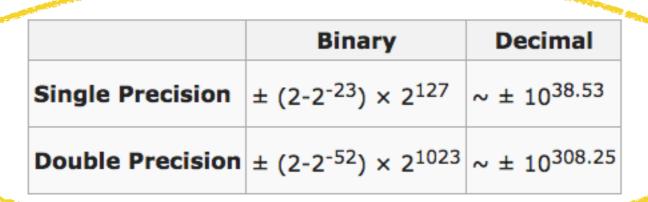
Range of Floating-Point Numbers

	Denormalized	Normalized	Approximate Decimal
Single Precision	$\pm 2^{-149}$ to $(1-2^{-23})\times 2^{-126}$	$\pm 2^{-126}$ to $(2-2^{-23})\times 2^{127}$	$\pm \sim 10^{-44.85}$ to $\sim 10^{38.53}$
Double Precision	$\pm 2^{-1074}$ to $(1-2^{-52})\times 2^{-1022}$	$\pm 2^{-1022}$ to $(2-2^{-52})\times 2^{1023}$	$\pm \sim 10^{-323.3}$ to $\sim 10^{308.3}$

Range of Floating-Point Numbers

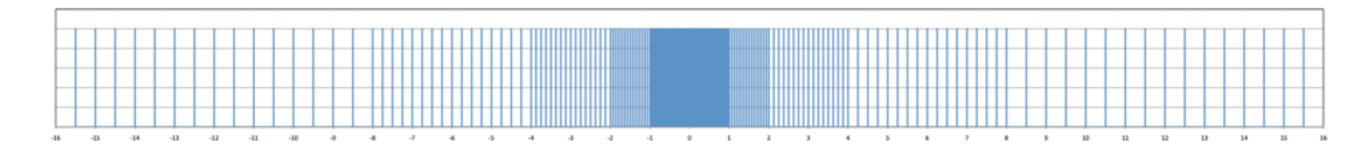
Remember that!

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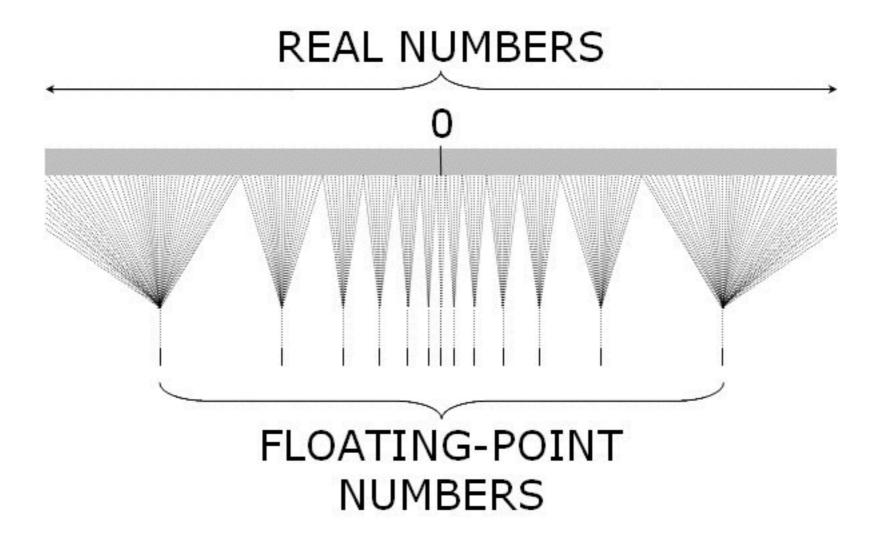


Resolution of a Floating-Point Format

Check out table here: http://tinyurl.com/FPResol



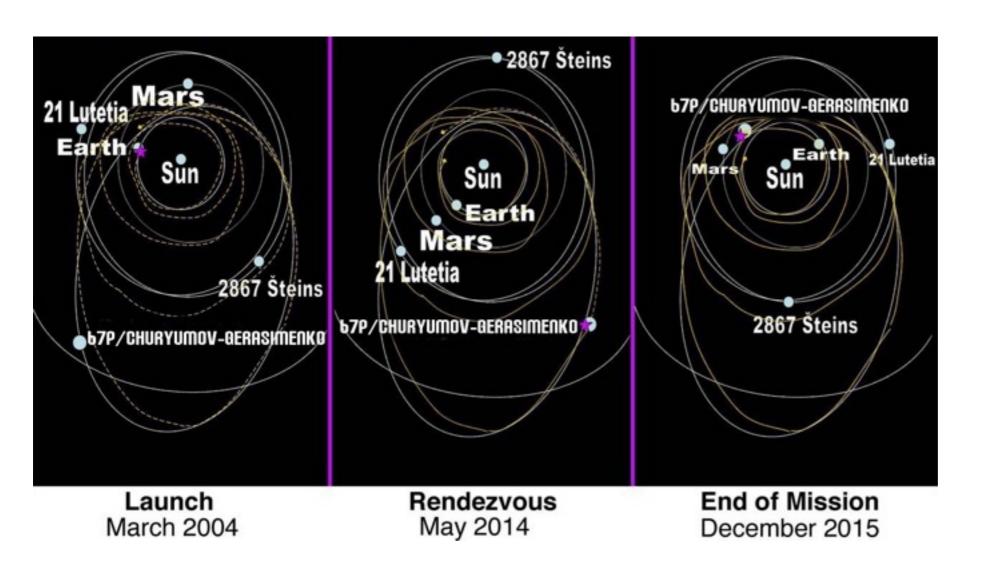
Resolution Another way to look at it



http://jasss.soc.surrey.ac.uk/9/4/4.html

What does it have to do with Art?





- Rosetta Landing on Comet
- 10-year trajectory

Why not using 2's Complement for the Exponent?

0.00000005

65536.5

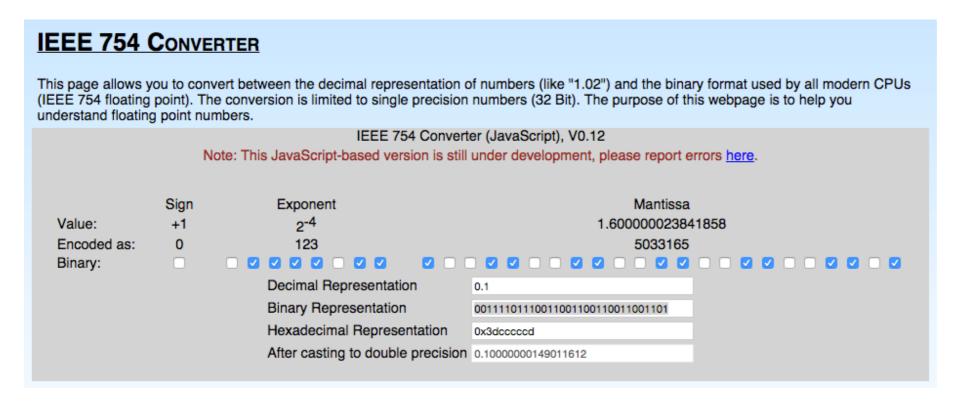
65536.25

= 0 **01100110** 101011010111111110010101

= 0 **10001111** 000000000000000001000000

= 0 **10001111** 000000000000000000100000





- Does this converter support NaN, and ∞?
- Are there several different representations of +∞?
- What is the largest float representable with the 32-bit format?
- What is the smallest normalized float (i.e. a float which has an implied leading 1. bit)?

How do we add 2 FP numbers?

- fp1 = s1 m1 e1 fp2 = s2 m2 e2fp1 + fp2 = ?
- denormalize both numbers (restore hidden 1)
- assume fp1 has largest exponent e1: make e2 equal to e1 and shift decimal point in m2 —> m2'
- compute sum m1 + m2'
- truncate & round result
- renormalize result (after checking for special cases)

$1.111 \times 2^5 + 1.110 \times 2^8$

$$1.111 \times 2^5 + 1.110 \times 2^8$$

after expansion

$$1.110000000 \times 2^{8} + 0.001111100 \times 2^{8}$$

locate largest number shift mantissa of smaller

1.11111100 x 28

compute sum

round & truncate

 $= 10.000 \times 2^{8}$

normalize

 $= 1.000 \times 2^{9}$

How do we multiply 2 FP numbers?

- fp1 = s1 m1 e1 fp2 = s2 m2 e2fp1 x fp2 = ?
- Test for multiplication by special numbers (0, NaN, ∞)
- denormalize both numbers (restore hidden 1)
- compute product of m1 x m2
- compute **sum** e1 + e2
- truncate & round m1 x m2
- adjust e1+e2 and normalize.

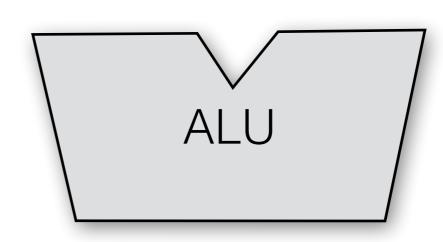
How do we compare two FP numbers?

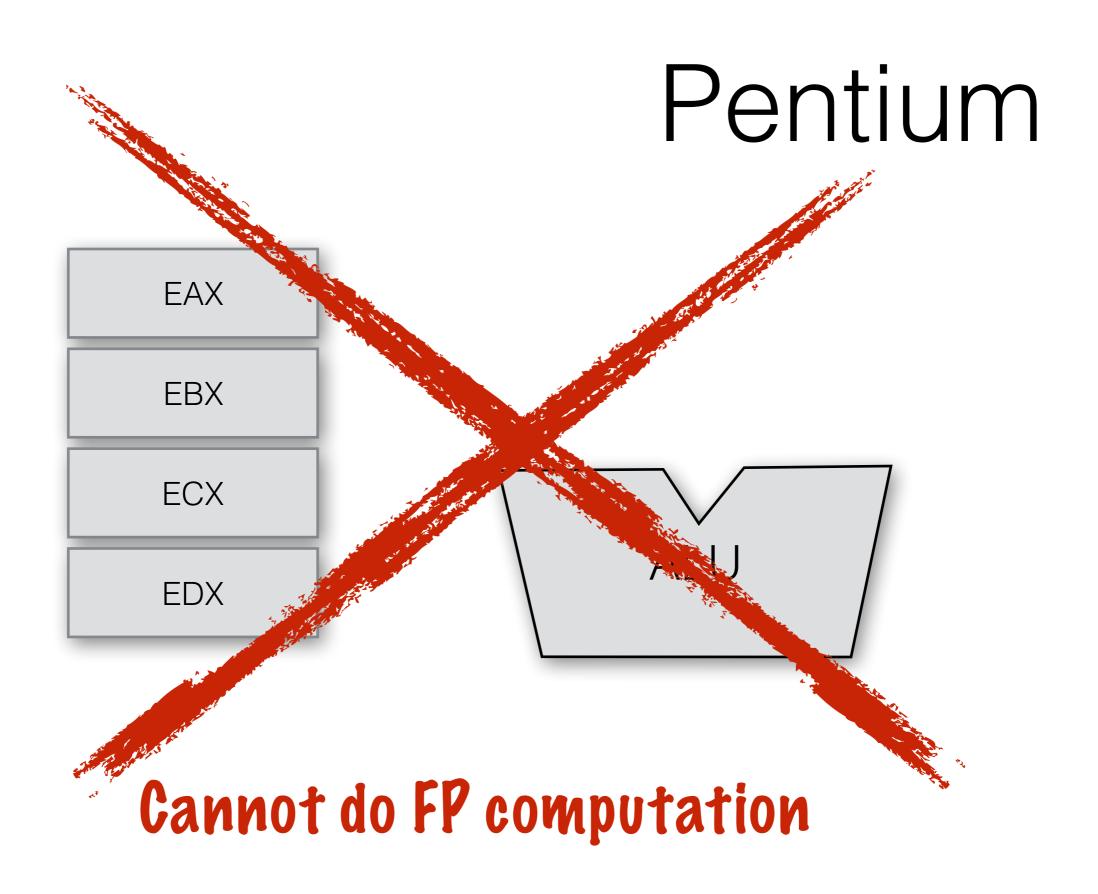
Check sign bits first, the compare as unsigned integers!
No unpacking necessary!

Programming FP Operations in Assembly...

Pentium







Intel Pentium 5 Prescott Trace Cache Access, **Instruction Trace Cache Execution Pipeline Start** Buffer Allocation & next Address Predict Register Rename Trace Cache Micro code Sequencer Register Alias History Tables (4x128) Fill Buffers Micro code Flash & ROM Register Alias Tables Instruction Queue (for less critical Trace Cache Branch Prediction fields of the uOps) Table (BTB), 1024 entries. General Instruction Address Queue & 16k uOps Memory Instruction Address Queue Return Stacks (4 x16 entries) 128 kByte (queues register entries and latency Trace Cache next IP's (4x) fields of the uOps for scheduling) 8 way set uOp Schedulers Instruction Decoder associative uControler 8 x 512 sets Parallel (Matrix) Scheduler RAM/ROM for the two double pumped ALU's of 4 uOps Up to 4 decoded uOps/cycle General Floating Point and (from max. one x86 instr/cycle) Slow Integer Scheduler: Instructions with more than four Tag comparators (8x8 dependency matrix) are handled by Micro Sequencer 39 bit virtual Tags FP Move Scheduler: Raw Instruction Bytes in (8x8 dependency matrix) Data TLB, 64 entry fully associative, between threads Load / Store Linear Address dual ported (for loads and stores) Collision History Table Load / Store uOp Scheduler: Front End Branch Prediction Tables (BTB), shared, 4096 (8x8 dependency matrix) entries in total Instruction TLB's 128 entry. FP, MMX, SSE1..3 fully associative for 4k and 4M pages. In: Virtual address [47:12] Floating Out: Physical address [39:12] + Point Floating Point, MMX, SSE1...3 2 page level bits Renamed Register File Registers 256 entries of 128 bit. Legacy Floating **Instruction** Fetch Integer Execution Core Point from L2 cache and Multiply (1) uOp Dispatch unit & Replay Buffer Legacy L2 Phys. **Branch Prediction** Dispatches up to 6 uOps / cycle 512 kByte 512 kByte L2 Cache Floating Tags (2) Integer Renamed Register File Pnt.Add L2 Cache L2 Cache 256 entries of 32 bit (+ 6 status flags) Transfer 12 read ports and six write ports L2 Phys Block Block Buffers (3) Databus switch & Bypasses to and from the Integer Register File. (4) Flags, Write Back Front Side Bus Inter-(5) Double Pumped ALU 0 face, 533..800 MHz (6) Double Pumped ALU 1 (7) Load Address Generator Unit (8) Store Address Generator Unit (11) ROB Reorder Buffer 4x64 entries

http://chip-architect.com/news/2003_04_20_looking_at_intels_prescott_part2.html

(14) Cache Line Read / Write Transferbuffers and

256 bit wide bus to and from L2 cache

(9) Load Buffer (96 entries)

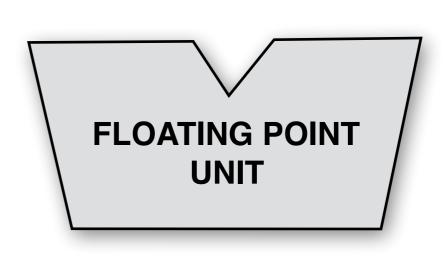
(10) Store Buffer (48 entries)

16 kByte Level 1 Data cache

four way set associative. 1R/1W

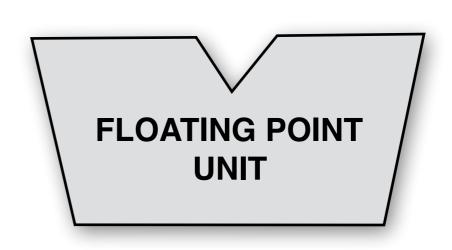
April 19, 2003 www.chip-architect.com

SP0 SP1 SP2 SP3 SP4 SP5 SP6 SP7



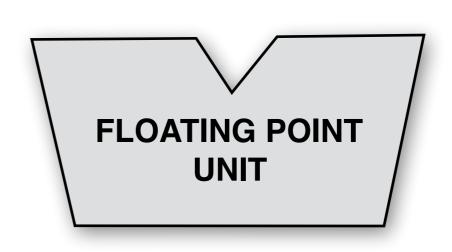
Operation: (7+10)/9

SP0 SP1 SP2 SP3 SP4 SP5 SP6 SP7



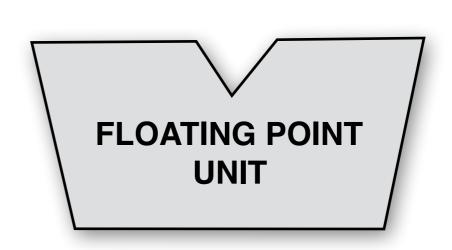
SP0 SP1 SP2 SP3 SP4 SP5 SP6 SP7

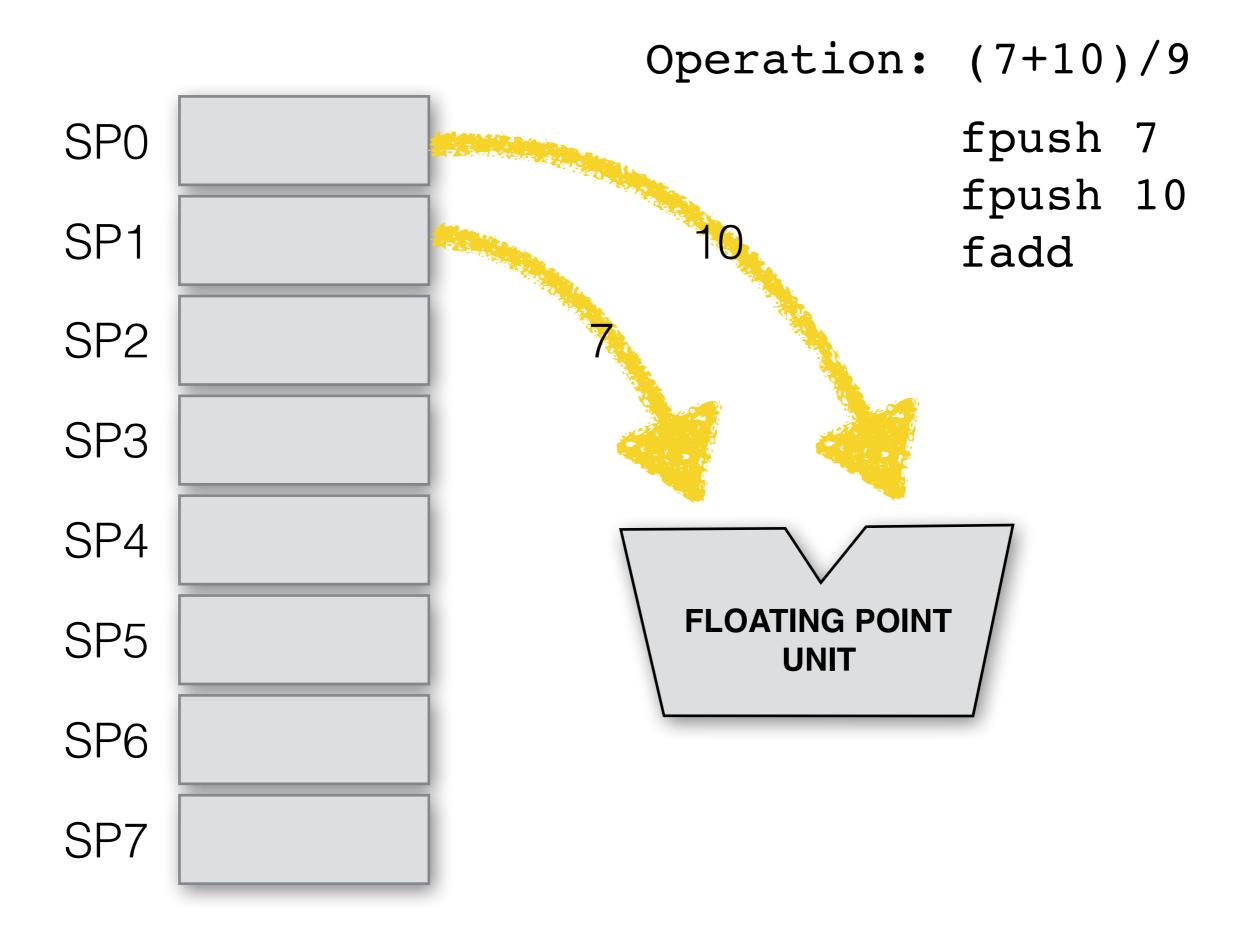
Operation: (7+10)/9 fpush 7

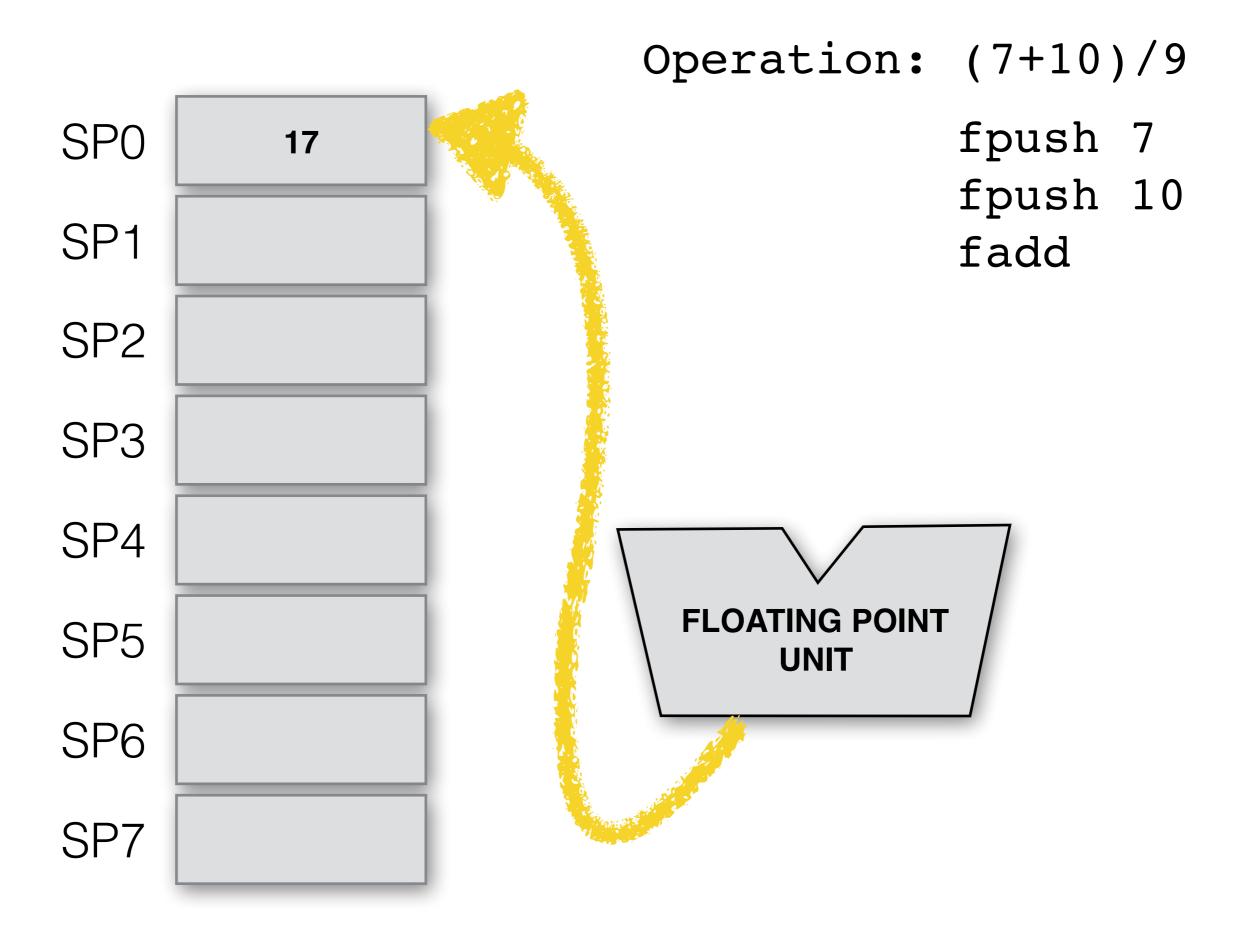


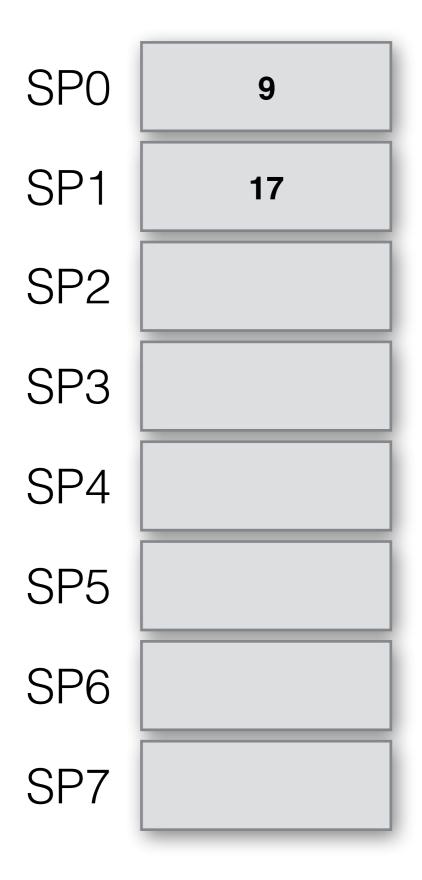
SP0 10 SP1 SP2 SP3 SP4 SP5 SP6 SP7

Operation: (7+10)/9 fpush 7 fpush 10

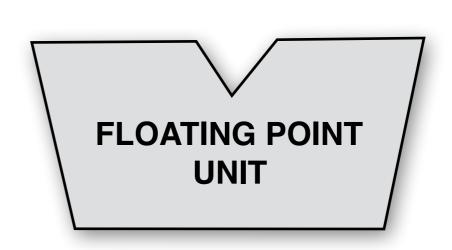


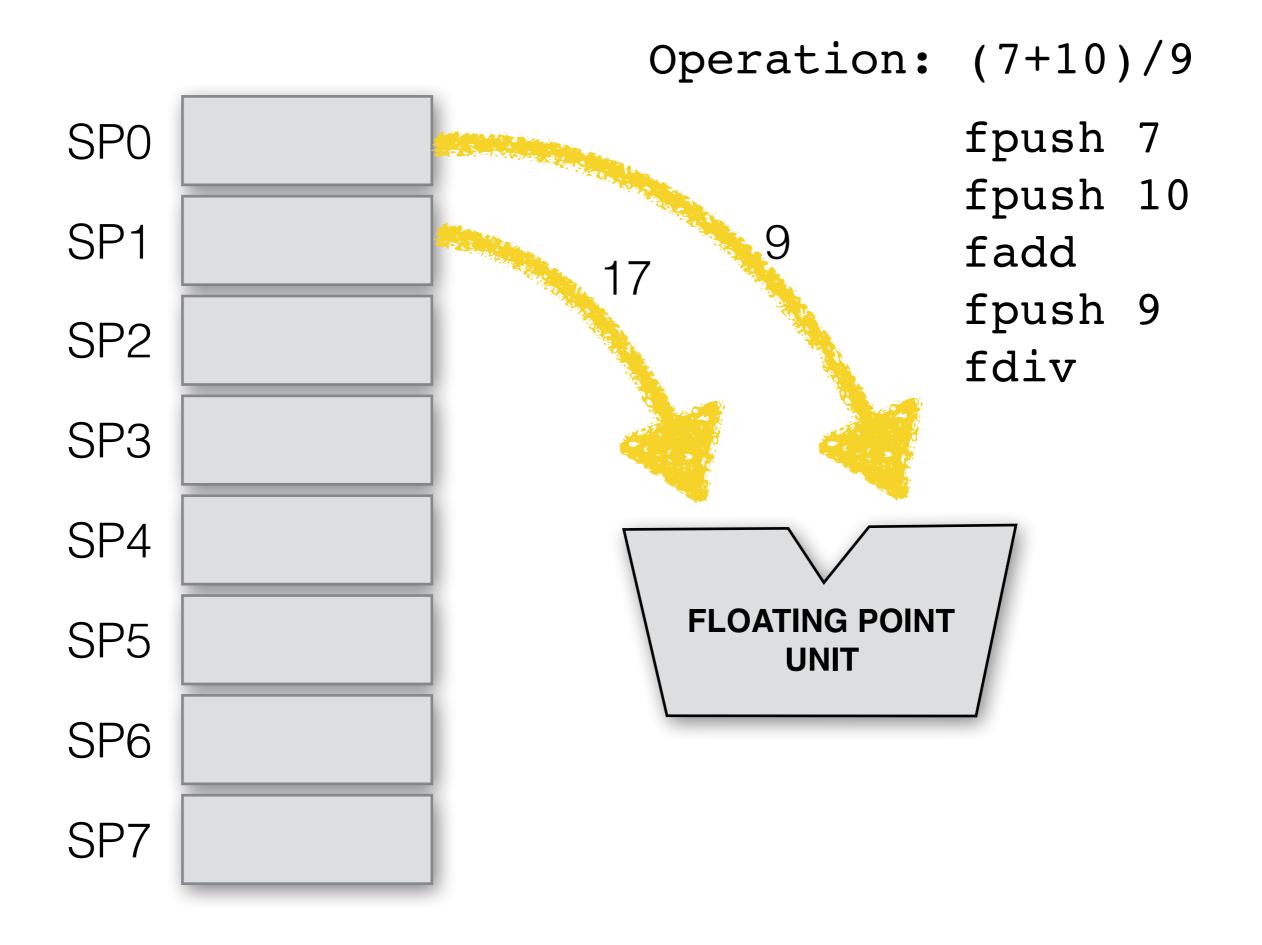


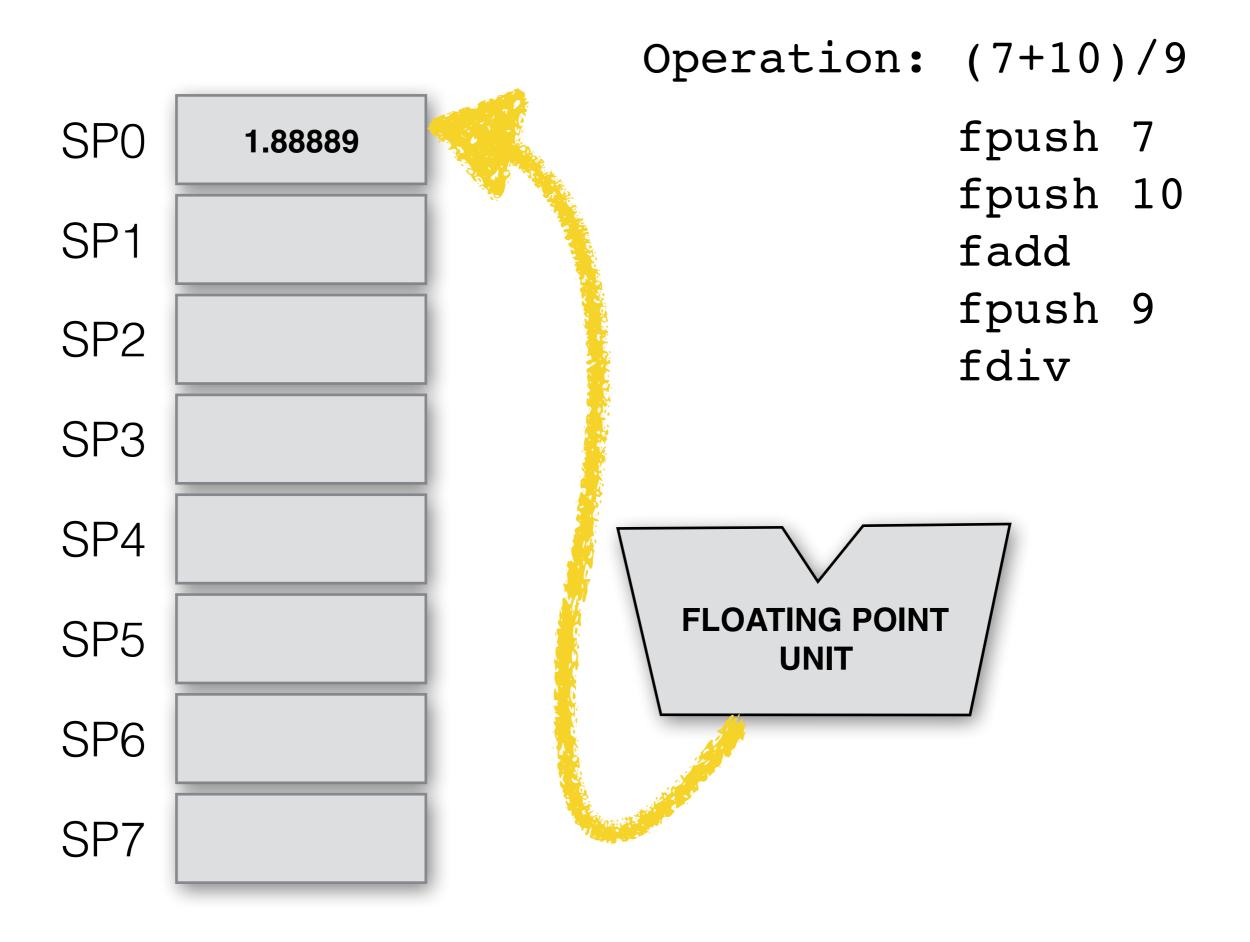




Operation: (7+10)/9 fpush 7 fpush 10 fadd fpush 9







The Pentium computes FP expressions using RPN!

The Pentium computes FP expressions using RPN! Reverse Polish Notation

Nasm Example: z = x+y

```
SECTION .data
      dd 1.5
X
      dd 2.5
У
      dd 0
; compute z = x + y
      SECTION .text
       fld dword [x]
      fld
             dword [y]
      fadd
      fstp dword [z]
```